

ABSTRACT

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5 A charge sampling (CS) circuit (1), comprising a control signal generator (4) for controlling an analog input signal to the charge sampling circuit (1) to be integrated by an intergrator (3) during a sampling phase responsive to a sampling signal from the control signal generator (4), wherein the current of the analog input signal is integrated to an integrated charge for producing a proportional voltage or current sample at a signal output

10 at the end of the sampling phase.

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